

## Matteo Lodi

Researcher

### EDUCATION AND TRAINING

September 2015

#### **Master degree**

Master's Degree in Electronic Engineering, University of Genoa

Final grade: 110/110 cum laude

Thesis title: "Design, implementation, and testing of a mixed analog/digital circuit emulating the behavior of biological neurons and small networks."

Università di Genova

September 2013

#### **Bachelor's Degree**

Bachelor's Degree in Electronic Engineering, University of Genoa

Final grade: 110/110 cum laude

Thesis title: "Circuit implementation of piecewise linear discontinuous control functions."

Università di Genova

2010-2015

#### **Advanced Training in ICT and Management**

Attended the "Advanced Training in ICT and Management" program at the Institute for Advanced Studies of the University of Genoa (ISSUGE)

Università di Genova

### PROFESSIONAL HISTORY

November 2018 – today

#### **Researcher**

Researcher at the Department of Naval, Electrical, Electronic, and Telecommunications Engineering (DITEN)

Università degli Studi di Genova.

Novembre 2015 – Ottobre 2018

#### **Ph.D**

Doctoral studies at the COMPSYS laboratory, University of Genoa

Supervisor: Prof. Marco Storace

Ph.D. in Science and Technology for Electrical Engineering, Naval Engineering, and Complex Systems for Mobility, curriculum in Electrical Engineering, XXXI cycle

Università degli studi di Genova.

## EXPERIENCE

### MAIN SEMINARS

[22-24 July 2024](#) Speaker at "IEEE workshop on complexity in engineering" (COMPENG),

[19-23 May 2019](#) Speaker at "SIAM Conference on Applications of Dynamical Systems" (DS19), Snowbird (USA)

[27-30 May 2018](#) Speaker at "IEEE International Symposium on Circuits and Systems" (ISCAS), Firenze (Italia)

[7-9 September 2017](#) Speaker at "New Generation of Circuits and Systems Conference" (NGCAS), Genova (Italia)

[12-15 June 2017](#) Speaker at "IEEE International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design" (SMACD), Giardini Naxos-Taormina (Italia).

[29-31 May 2017](#) Speaker at "11th Symposium on Hysteresis Modeling and Micromagnetics "(HMM), Barcellona (Spagna)

### EDITORSHIP OR PARTICIPATION IN EDITORIAL BOARDS OF JOURNALS, PUBLISHING SERIES, ENCYCLOPAEDIAS AND TREATISES

[2023-2025](#) Associate editor per l' IEEE Transactions on Circuits and Systems I: Regular Papers.

[2021-2023](#) Associate editor for the International Journal of Circuit Theory and Applications.

[2022](#) Track co-chair per ISCAS'22. Track "Nonlinear Systems and Circuits Theory".

[2021, 2023-2025](#) Review Committee Member for ISCAS'21, ISCAS'23, ISCAS'24 and ISCAS'25;

### PRIZES AND ACCOLADES FOR SCIENTIFIC ACTIVITY, INCLUDING MEMBERSHIP OF ACADEMIES

[2017](#) - Demo Competition Award for the contribution "A Low-Cost Free-Space Optical Communication Prototype", presented at Ngcas'17.